

An Autonomous Institute
Dhulapally, Secunderabad – 500100
NBA & NAAC A+ Accredited



Department of Electronics & Communication Engineering

TIME - TABLE

Year : IV B.Tech Semester I

Section: A Academic Year: 2020-2021 W.e.f: 24/08/2020 Class In charge: Mrs.A.Soumya

Mode of Teaching: Zoom, Skype, Google Hangouts and other online teaching

		09:30 AM	10:10 AM	10:50AM	11:0	00 AM	11:40 AM	
WOVE.		to 10:10 AM	to 10:50 AM	to 11:00AM	11:	to 40 AM	to 12:20 PM	
HOUF	ł	2012012112	2000 0 12012	111001111	11.	10 121/2	22/20 23/2	
DAY		1	2		3		4	
MON		VLSI D	EMI		MV	VE	CN	
		AS	GS		G	U	CK	
		ESD	VLSI D	В	CN	1	MWE	
TUE		MV	AS	R	СЕ	X.	GU	
		EMI	ESD		Cì	N	VLSI D	
WED		GS	MV	E	Cl	K	AS	
		MWE	VLSI D	\mathbf{A}	EM	⁄II	ESD	
THU		GU	AS	••	G:	S	MV	
		ESD	EMI	K	MW	VE	VLSI D	
FRI		MV	GS		GU	J	AS	
		EMI	MWE		ES	D	CN	
SAT		GS	GU		MV		CK	
MWE CN ESD EMI	: : : : : : : : : : : : : : : : : : : :	Microwave Engineerin Computer networks Embedded System Des Electronic Measureme	sign	GU CK MV GS	G.UPENDER Dr.CHAITHANYA KRISHNA M.VENKANNA Dr.G.SRIDHAR			
VLSI D	:	Instrumentation VLSI Design	nts and	AS	A.SOUMYA			
CHIEF IN-CHARGE (TIME TABLE)				HOD-ECE		Pl	RINCIPAL	



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Department of Electronics & Communication Engineering

TIME - TABLE

Year : IV B.Tech Semester I
Section : B Academic Year: 2020-2021
W.e.f : 24/08/2020 Class In charge: Mr. K.Karthik
Mode of Teaching: Zoom, Skype, Google Hangouts and other online teaching

HOUR		09:30 AM to 10:10 AM	10:10 AM to 10:50 AM		10:50AM to 11.00AM		11:00 AM to 11:40 AM	11:40 AM to 12:20 PM	
DAY		1	2				3	4	
MON		VLSI D	CN				EMI	ESD	
		GRR	DP				TS	KK	
		MWE	EMI		B VLSI D		ESD		
TUE		AA	TS		n		GRR	KK	
		EMI	VLSI D		R		CN	MWE	
WED		TS	GRR		${f E}$		DP	AA	
		ESD	MWE		A	,	VLSI D	CN	
THU		KK	AA			(GRR	DP	
		MWE	CN		K		EMI	ESD	
FRI		AA	DP				TS	KK	
		VLSI D	MWE				ESD	EMI	
SAT		GRR	AA				KK	TS	
MWE CN	:	Microwave Engineerin Computer networks	g		AA DP		Dr.A.ANAND D.PRASAD		
ESD	:	Embedded System Design KK K.KARTHIK		RTHIK					
EMI	:	Electronic Measurements and		TS	T.SOW	T.SOWMYA			
		Instrumentation							
VLSI D	VLSI D : VLSI Design				GRR G.RA		G.RAMESH REDDY		
CHIEF	CHIEF IN-CHARGE (TIME TABLE)				HOD-ECE	,	PRI	INCIPAL	



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Department of Electronics & Communication Engineering

TIME - TABLE

Year : IV B.Tech Semester I
Section : C Academic Year: 2020-2021
W.e.f : 24/08/2020 Class In charge: Mr.K.Nishakar
Mode of Teaching: Zoom, Skype, Google Hangouts and other online teaching

EMI KN CN SS VLSI D GV ESD KR MWE	MWE TSK ESD KR MWE TSK EMI KN	B R E A K	3 CN SS VLSI D GV EMI KN MWE TSK	ESD KR MWE TSK ESD KR VLSI D GV	
KN CN SS VLSI D GV ESD KR	TSK ESD KR MWE TSK EMI KN	R E A	SS VLSI D GV EMI KN MWE	KR MWE TSK ESD KR VLSI D	
CN SS VLSI D GV ESD KR	ESD KR MWE TSK EMI KN	R E A	VLSI D GV EMI KN MWE	MWE TSK ESD KR VLSI D	
SS VLSI D GV ESD KR	KR MWE TSK EMI KN	R E A	GV EMI KN MWE	TSK ESD KR VLSI D	
VLSI D GV ESD KR	MWE TSK EMI KN	E A	EMI KN MWE	ESD KR VLSI D	
GV ESD KR	TSK EMI KN	A	KN MWE	KR VLSI D	
ESD KR	EMI KN	A	MWE	VLSI D	
KR	KN				
		- K	TSK	GV	
MWE		K			
	VLSI D		EMI	CN	
TSK	GV		KN	SS	
VLSI D	CN		ESD	EMI	
GV	SS		KR	KN	
Microwave Engineering		TSK	T.SUDHEER KUMAR		
Computer networks		SS	S.SANTOSH REDDY		
Embedded System Desi	ign	KR	K.RAJU K.NISHAKAR		
Electronic Measuremen	its and	KN			
nstrumentation					
LSI Design		GV	GV G.VINATHA		
i i	omputer networks mbedded System Desi lectronic Measuremen strumentation	omputer networks mbedded System Design lectronic Measurements and strumentation	omputer networks SS mbedded System Design lectronic Measurements and strumentation SS KR KN	omputer networks SS S.SANTOSH RED KR kectronic Measurements and strumentation KR KRAJU KN KNISHAKAR	



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Department of Electronics & Communication Engineering

TIME - TABLE

Year : IV B.Tech Semester I
Section : D Academic Year: 2020-2021
W.e.f : 24/08/2020 Class In charge: Mr.G.Vikram
Mode of Teaching: Zoom, Skype, Google Hangouts and other online teaching

HOUR	09:30 AM to 10:10 AM	10:10 AM to 10:50 AM	10:50AM to 11.00AM	1	00 AM 00 AM	11:40 AM to 12:20 PM	
DAY	1	2			3	4	
	MWE	ESD		VLS	I D	EMI	
MON	SRK	KNV		PP		GV	
	ESD	VLSI D	В	EMI	-	CN	
TUE	KNV	PP	ightharpoonupR	GV		SPM	
	CN	EMI		VLSI	D	MWE	
WED	SPM	GV	E	PP		SRK	
	EMI	MWE	\mathbf{A}	VLSI	D	ESD	
THU	GV	SRK	T 7	PP		KNV	
	VLSI D	CN	K	MW	E	ESD	
FRI	PP	SPM		SRK		KNV	
	CN	EMI		ESD		MWE	
SAT	SPM	GV			7	SRK	
MWE	: Microwave Engineerin	ıg	SRK	SPM S.P.MANIKANTA			
CN	: Computer networks		SPM				
ESD	: Embedded System De	sign	KNV				
EMI		Electronic Measurements and		G.VIKRAM			
	Instrumentation	Instrumentation					
VLSI D	: VLSI Design		PP P.PU		PUSHPA		
CHIEF	IN-CHARGE (TIME T	TABLE)	HOD-ECE		р	RINCIPAL	